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Subject : Electronics	Semester: 5th	(PAPER: CC -XI)
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MODEL QUESTION

GROUP ~(A)

Mul

tiple choice Question:		
(1) Which type of RAM need regula	ar referred	
3	b. static RAM	
b. c. permanent RAM		
(2) Eight of the register are known		
a. general purpose register		nter or index registers
c. segment registers	d. othe	er register
(3) ALE stands for		
a. address latch enable		b. address light enable
c. address lower enable		d. address last enable
(4) In 8085, 16- bit address bus , wh	ich can address	s upto ?
a.16 kb b. 32kb	c. 64kb	d. 128 kb
(5) It is also a 16-bit register works push & POP operations.	like stack, whi	ch is always incremented/decremented by 2 during
a. stock pointer	b. temporary	register
c. flag register d. prog	gram counter	
(6) Consider the following registers	:	
a. Accumulator and flag regi	ster	b. B and C register
c. D and C register		d. H and L register

	Which of these 8-bit register of 8085 microprocessor can be paired together to make a16-bit register?							
	a. 1,3 and 4	b. 2,3 and 4	C	c. 1,2 and 3	d. 1,2 and 4			
(7)In 8	8086 micropro	cessor system with me	emory map	oped I/O ,which	of the following is true?			
	a. devices have 8-bit address line							
	b. devices are accessed using IN and OUT							
	c. there can be maximum of 256 input devices and 256 output devices							
	d. arithmetic and logic operation can directly performed with the I/O							
(8) Consider the following:								
	1. sign flag	2. Trap flag	C	e. pority flag	d. auxiliary flag			
	a. 1only	b. 1 and 2	c. 2 and	3 d. 1, 3 an	d 4			
(9) In	an intel 8085A	microprocessor, why	is READ	Y signed used?				
	a. to include t	to user that the microp	processor	working and is	ready for use			
	b. to provide proper WAIT states when the microprocessor is communicating with a slow peripheral device							
	c. to slow down a fast peripheral device so as to communicate at the microprocessor's							
	d. none of the	e above						
(10) A	LU (Arithmet	tic and Logic unit) of 8	085 micro	oprocessor cons	ists of			
a. accumulator, temporary register, arithmetic and logic circuits								
	b. accumulator ,arithmetic ,logic circuits and five flags							
	c. accumulator ,arithmetic and logic circuits							
d. accumulator ,temporary register, arithmetic, logic circuits and five flags								
(11) In 8085 microprocessor , how many interrupts are maskable								
	a. two	b. three	b. four	d. five				
(12) Which stack is used in 8085 microprocessor								

(13) In the instruction of the 8085 microprocessor , how many bytes are present?					
a. one or two	b. one, two or three	c. two or three	e d. one olny		
(14) Which one of the	following register of 8085	microprocessor is not a	part of the programming		
Model?					
a.instruction re	gister b.memory a	ddress register			
c. status registe	er d. temporar	y data register			
(15) The program coun	nter in 8085 microprocesso	or is a 16 bit register, be	cause		
a. it cou8nts 16	a. it cou8nts 16 bits at a time				
b. there are 16	b. there are 16 bits address times				
c. it facilitate th	c. it facilitate the users storing				
d. it has to fetch	d. it has to fetch two 8 bit data at a time				
(16) A direct memory a	access (DMA) transfer rep	blies			
a. direct transfer of data between memory and accumulator					
b. direct transfer of data between memory and I/O devices without the use of microprocessor					
c. transfer of data exclusively within microprocessor					
d. a fast transfe	d. a fast transfer of data between microprocessor and I/O devices				
(17) In a microprocessor, the address of the new next instruction to be executed is stored in					
a. stack pointer	b. address latch	c. program counter	d. general purpose register		
(18) The instruction R	EI executes with the follow	wing series of machine c	ycle		
a. fetch, read, w	rite b. fetch, wri	te, write			
c. fetch, read, re	ead d. fetch ,read	d			
(19) Which of the follo	wing statements is correct	t regarding the instructi	on CMP A-		
a. compare accumulator with register A					
b. compare accı	ımulator with memory				

c. LIIO

d. LILO

a. FIIO

.b. FILO

- c. compare accumulator with register H
- d. this instruction does not exist
- (20) The instruction JNC 16- bit refers to jump to 16-bit address if?
 - a. sign flag is set
- b. carry flag is reset
- c. zero flag is set
- d. parity flag is reset

GROUP-B

Short answer type questions:

- (1) What is registers in 8085 microprocessor?
- (2) What is program counter (PC)?
- (3)Draw the pin diagram of 8085 microprocessor?
- (4) What is memory in 8085 microprocessor? Explain its types?
- (5) Write down about 8085 microprocessor?
- (6) Classify the instruction set on according to word size.
- (7) What is stock and subroutine?
- (8) What is the branch instruction also write its type?
- (9)Draw the timing diagram for memory? Writ its operation.
- (10) What is data transfer (copy) operation? Write its instructions

GROUP-C

Long answer type question

- (1) What is addressing modes? Explain its type briefly with suitable examples.
- (2)Draw and explain the architecture of the 8085 microprocessor.
- (3)Problem statement: Write a program to perform the following functions and verify the output.
 - a. Load the number 8BH in register D.
 - b. Load the number 6FH in register C.
 - c. Increment the contents of registers C and D and display the sum at the output PORT 1.
- (4)Draw and explain the timing diagram for memory read operation.
- (5) Classify the memory also explain static memory (SRAM) and dynamic memory (DRAM).
- (6) Write a program to the following.
 - a. load the number 30 H in register B and 39 H in register C.
 - b. substract 32 H from 30H
 - c. display the answer at PORT1.